

Cluster Memory contains 23 documents.

1. Citations: Efficient Utilization of Scratch-Pad Memory in Embedded Processor... [new window] [frame] [preview]

... reuse when considering the **access pattern** of each processor in ... in full control of the flow of **data** between onchip and off chip ... so it is relatively easy to **predict data access** times. Previous ...

URL: citeseer.com/context/421559/0 - show in clusters

Sources: Looksmart 2

2. NVIDIA nForce DDR chipset [new window] [frame] [preview]

... **accessing** and is able to **predict and access data**. This ... allows a processor to retrieve **data** directly from the **memory** in ... and tries to anticipate a **access pattern** based on some algorithms ...

URL: www.ixbt-labs.com/articles/nvidianforce - show in clusters

Sources: Looksmart 3

3. Citations: An effective on-chip preloading scheme to reduce data access penalty - Baer, Chen (ResearchIndex) [new window] [frame] [preview]

J-L. Baer and T-F. Chen. An effective on-chip preloading scheme to reduce **data access** penalty. In Proceedings of Supercomputing '91, Albuquerque, NM, November 1991. ... compiler can statically **predict** which **memory** references ... **data** when a miss occurs. Stride based **accesses** can be accommodated by hardware prefetching because the **memory access pattern** ...

URL: citeseer.nj.nec.com/context/31464/0 - show in clusters

Sources: MSN 9

4. Performance of On-Line Learning Methods in Predicting Multiprocessor Memory Access Patterns [new window] [frame] [preview]

Performance of On-Line Learning Methods in Predicting Multiprocessor Memory Access Patterns Majd F. Sakr , Steven P. Levitan , Donald M. Chiarulli , Bill G. Horne , C. Lee Giles NEC Research ...

URL: www.neci.nj.nec.com/....multiprocessor.memory.prediction.pdf - show in clusters

Sources: MSN 28

5. Microsoft PowerPoint - 6C-2.ppt [new window] [frame] [preview]

... to include more loop optimizations, such as loop interchange and loop unrolling A model to **predict** the **memory access** cost based on **data access pattern** is under ...

URL: www.csis.hku.hk/cluster2003/presentation/technical/6C-2.pdf - show in clusters

Sources: Netscape 7

6. Stream Mechanism [new window] [frame] [preview]

... to **predict** the **memory access pattern** at runtime, MagicEight proposes decoupling **memory accesses** from **data processing** ...

URL: web.media.mit.edu/~wad/tp/node10.html - show in clusters

Sources: MSN 23

7. Sun BluePrints OnLine - Archives By Subject [new window] [frame] [preview]

... strategy that enables you to **predict** and correct potential ... than main **memory**. **Memory access** time is increasingly the ... amount of time waiting for **data**. This not only negatively ...

URL: www.sun.com/solutions/blueprints/browsesubject.html - show in clusters

Sources: Looksmart 24

8. Introduction [new window] [frame] [preview]

... overlap processor computation with **data access** , is one of the ... study the complex **memory access pattern** at compile time and ... not be able to **predict** complicated **memory access patterns**. ...

URL: www-cad.eecs.berkeley.edu/~roby/cs252/paper/node1.html - show in clusters

Sources: MSN 43

9. Analyzing memory Access patterns of Programs on Alpha-based Architectures:memory access profiling tool, memory access pa [new window] [frame] [preview]

... The ability to understand or **predict** the execution path without looking ... reuse of **data** in the user's program.

In this paper, we investigate the **memory access pattern** of Fortran ...

URL: www.research.digital.com/.../DTJ/DTJS02/DTJS02HM.HTM - show in clusters

Sources: MSN 50

10. www.vldb.org/conf/2002/S06P03 [new window] [frame] [preview]

... different **data access pattern** . This means ... function to **predict** its cache ... hence to **predict** their **memory access costs**. The ... combine **basic pattern** to compound ... execution of **data** ...

URL: www.vldb.org/conf/2002/S06P03.pdf - show in clusters

Sources: Lycos 12

11. [Dynamic Management of Scratch-Pad Memory Space](#) [new window] [frame] [preview]

Dynamic Management of Scratch-Pad **Memory Space** M. Kandemir, J. Ramanujam , M. J. Irwin, N. Vijaykrishnan, I. Kadayif, and A. Parikh Microsystems Design Lab The Pennsylvania State University **memory**, so it is relatively easy to **predict data access** . times. Previous work on SPM[10] investigates ... **memory**, the applica- . **tion access pattern** , and the available **memory space** in ...

URL: jamaica.ee.pitt.edu/.../papers/2001/dac01/pdffiles/42_1.pdf - show in clusters

Sources: MSN 53

12. [Probert Encyclopaedia: Science & Technology \(A\)](http://www.probertencyclopaedia.com/SA.HTM) [new window] [frame] [preview]

... directly to the computer's **memory**, allowing **data** to be transferred directly between **memory** and ... for detecting vibrations in machinery. **ACCESS CHARGE Access Charge** is a cost assessed to ...

URL: www.probertencyclopaedia.com/SA.HTM - show in clusters

Sources: Looksmart 33

13. [Characterization of Repeating Data Access Patterns in Integer Benchmarks](http://www.crhc.uiuc.edu/.../ftp/conference/iscampiw-01-pattern.pdf) [new window] [frame] [preview]

Characterization of Repeating **Data Access Patterns** in Integer Benchmarks Erik M. Nystrom Roy Dz-ching Ju Wen-mei W. Hwu Processor speeds continue to outpace the **memory** subsys- tem making it However, the **pattern** useful for preventing B from missing is con- ... addresses to **predict** the next address. ...

URL: www.crhc.uiuc.edu/.../ftp/conference/iscampiw-01-pattern.pdf - show in clusters

Sources: MSN 8

14. [Quantifying and Resolving Remote Memory Access Contention on Hardware DSM Multiprocessors](http://www.cs.wm.edu/~dsn/papers/ipdps02_2.pdf)

[new window] [frame] [preview]

Quantifying and Resolving **Remote Memory Access** Contention on Hardware DSM Multiprocessors Dimitrios S. Nikolopoulos Coordinated Science Laboratory University of Illinois at Urbana-Champaign 1308 we are able to **predict** the impact of remote mem- ... the mem- . **ory access pattern** of the program throughout ... During the **memory access** phase,.. each thread retrieves **data** from **memory**

URL: www.cs.wm.edu/~dsn/papers/ipdps02_2.pdf - show in clusters

Sources: MSN 68

15. [C](#) [new window] [frame] [preview]

USC CSCE TR-2002-001. Short paper presented at Work in Progress Session at The 11th International Conference on Parallel Architectures and Compilation Techniques (PACT-02), September, 2002. Thus I is a function of **data** . **size (s)** and **access pattern (d)** computing only have few non-contiguous **data access patterns**.. we can **predict** **memory** communication latency (l) for ...

URL: www.cse.sc.edu/~kcameron/prof/papers/pactwip02.pdf - show in clusters

Sources: MSN 93

16. [Papers - Sixth USENIX Security Symposium](http://www.usenix.org/.../proceedings/sec96/full_papers/gutmann) [new window] [frame] [preview]

Sixth USENIX Security Symposium 77-90 of the Proceedings Secure Deletion of **Data** from Magnetic and Solid- State **Memory** ... wishing to gain **access** to sensitive **data** is forced to ... of supposedly erased **data** from magnetic media or random- **access memory** involved writing a **fixed pattern** of all 1's ...

URL: www.usenix.org/.../proceedings/sec96/full_papers/gutmann - show in clusters

Sources: MSN 61

17. [Using Whole-program Locality to Predict Cache Miss Rate](#) [new window] [frame] [preview]

Using Whole-program Locality to **Predict Cache Miss Rate** Yutao Zhong, Steven G. Dropsho, and Chen Ding Computer Science Department University of Rochester Improving cache performance requires capability to **predict** . the **memory** reference **pattern** for a ... program **access** behavior to a. fully associative cache. Ding and

Zhong analyzed the reuse. **pattern of data elements** [7]. ...

URL: www.cs.rochester.edu/~dropsho/papers/pact03_1.pdf - show in clusters

Sources: MSN 39

18. PowerPC G5 Performance Primer [new window] [frame] [preview]

... knows the **data usage pattern** in advance. Unlike ... the overhead to **access memory**. Adjust to the ... enough to map 512K of **data**, the same size as the ... suffixes to statically **predict highly predictable** ...

URL: aktuality.prolidi.net/...6-2003_apple_-_srovnani_g4_-_g5.php - show in clusters

Sources: Looksmart 81

19. Increasing the Accuracy of Data Prefetching Streams [new window] [frame] [preview]

... structures to. **predict** future **memory** addresses based on ... **data structures** around quite of-. ten. This can make prefetching techniques that are based on. learning the **access pattern** ...

URL: www.ucop.edu/research/micro/00_01/00_010.pdf - show in clusters

Sources: MSN 62

20. Welcome to the Redwood Neuroscience Institute [new window] [frame] [preview]

... of processing for **pattern** matching and forming ... and to associatively **access memories**, in the ... in methods of analyzing **data** from neuroimaging ... system which learns to **predict** its own sensory input ...

URL: www.rni.org/sci-staff.html - show in clusters

Sources: Looksmart 91

Result Pages: 1-20 - [21-23](#)

Cluster Memory contains 13 documents.

1. Quantifying Locality Effect in Data Access Delay: Memory LogP [new window] [frame] [preview]

... few non-contiguous data access patterns, we can predict memory communication ... levels making additional delays dependent upon the data access pattern and data ...

URL: www.cs.iit.edu/~scs/psfiles/KSipdps03.PDF - show in clusters

Sources: Netscape 3

2. On-Line Prediction of Multiprocessor Memory Access Patterns [new window] [frame] [preview]

... be effective, it has to predict the steps ... 400 Memory module # Time Communication data pattern (win = 0 ... 3: (a) The memory access pattern of the temperature ...

URL: external.nj.nec.com/.../ICNN96.multiprocessor.prediction.pdf - show in clusters

Sources: Netscape 17

3. Dynamic Management of Scratch-Pad Memory Space [new window] [frame] [preview]

... is relatively easy to predict data access. times. Previous work ... tion access pattern , and the available memory space in the ... the cost incurred in accessing the off-chip memory ...

URL: jamaica.ee.pitt.edu/.../papers/2001/dac01/pdffiles/42_1.pdf - show in clusters

Sources: MSN 40

4. Microsoft PowerPoint - 6C-2.ppt [new window] [frame] [preview]

... to include more loop optimizations, such as loop interchange and loop unrolling A model to predict the memory access cost based on data access pattern is under ...

URL: www.csis.hku.hk/cluster2003/presentation/technical/6C-2.pdf - show in clusters

Sources: Netscape 15

5. Performing File Prediction with a Program-Based Successor Model [new window] [frame] [preview]

... accessing pattern . If a match found,, files in that pattern tree are prefetched to memory. Vitter, Curewite, and Krishnan adopt the technique. of data compression to predict ...

URL: csl.cse.ucsc.edu/Papers/yeh-mascots01.pdf - show in clusters

Sources: MSN 15

6. Generic Database Cost Models for Hierarchical Memory Systems [new window] [frame] [preview]

Generic Database Cost Models for Hierarchical Memory Systems Martin L. Kersten CWI, Kruislaan 413, 1098 SJ Amsterdam, The Netherlands Accurate prediction of operator execution time is a used to predict the amount of data that each ... access pattern . Sequentially reading or writing consecutive pages causes. less cost per page than accessing scattered ...

URL: www.vldb.org/conf/2002/S06P03.pdf - show in clusters

Sources: MSN 64

7. A New Theoretical Framework For Explicit and Implicit Memory [new window] [frame] [preview]

... and indicate why we think the framework discussed here offers a better way of explaining available data. We then consider the problems associated with the measurement of so-called explicit and implicit ...

URL: psyche.cs.monash.edu.au/v3/psyche-3-02-mayes.html - show in clusters

Sources: Looksmart 61

8. Citations: A Data Locality Optimization Algorithm - Wolf, Lam (ResearchIndex) [new window] [frame] [preview]

Michael E. Wolf and Monica S. Lam. A Data Locality Optimization Algorithm. Proceedings of the ACM SIGPLAN Symposium on Programming Language Design and Implementation, pages 30--44, June 1991. ... of data loaded before in the level of the memory hierarchy under study is accessed without necessity of accessing the ... an easy to predict pattern and therefore the ...

URL: citeseer.nj.nec.com/context/288226/0 - show in clusters

Sources: MSN 26

9. HiDISC: A Decoupled Architecture for Applications in Data Intensive Computing [new window] [frame] [preview]

HiDISC: A Decoupled Architecture for Applications in Data Intensive Computing Drs. Alvin Despain and Jean-Luc Gaudiot The ever growing speed gap between processor and main memory has been a major on the

access pattern being **predicted** and fail ... the future **data** addresses. This is very. difficult to **predict** when the ... of generating addresses, **accessing memory**, and prefetching is ...

URL: pascal.eng.uci.edu/projects/HiDISC/Final_report.pdf - show in clusters

Sources: MSN 74

10. Computer Almanac - Numbers About Computers [new window] [frame] [preview]

... 2003 "Jupiter Research **predicts** that 28 million US households ... number of interconnected **data** -centric devices is a corresponding ... Kehoe, "Drowning in a Deluge of **Data**" Financial Times, p ... of...

URL: WWW.cs.cmu.edu/afs/cs.cmu.edu/user/bam/www/numbers.html - show in clusters

Sources: Lycos 17

11. Conserving Battery Energy through Making Fewer Incorrect File Predictions [new window] [frame] [preview]

... **accessing pattern** observed. Vitter, Curewite,.. and Krishnan adopt the technique of **data** compression. to **predict** next ...

URL: WWW.cse.ucsc.edu/~sbrandt/papers/WPMRTES.pdf - show in clusters

Sources: MSN 42

12. Concurrency and Computation:Practice and Experience [new window] [frame] [preview]

... on each image -- which forms a separate virtual **data** space. Multi-wavelength images can be used for ... for distributed computation by using a **bridge pattern** code synthesizer. CentiJ reuses original ...

URL: aspen.ucs.indiana.edu/CandCPandE/index.html - show in clusters

Sources: Looksmart 44

13. Grid computing made simple - The Industrial Physicist [new window] [frame] [preview]

... as opposed to **accessing data**, another ... such as fractals and **pattern** formation. FEM ... communicate and share **data** with its neighbors ... Use algorithms to **predict** the optimal number of ...

URL: www.tipmagazine.com/tip/INPHFA/vol-9/iss-4/p31.html - show in clusters

Sources: Looksmart 63

Top 58 results retrieved for the query **Predictor Directed Stream buffer sherwood** ([Details](#))

1. Predictor - Directed Stream Buffers - Sherwood, Sair, Calder ... [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... author = "Timothy **Sherwood** and Suleyman Sair and Brad Calder", title = " **Predictor - directed stream buffers** ", booktitle = "International Symposium on ...

URL: citeseer.nj.nec.com/sherwood00predictordirected.html - show in clusters

Sources: Netscape 1, MSN 1

2. Advance Program for the 33rd International Symposium on Microarchitecture [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Row-buffer Conflicts and Exploit Data Locality (Presentation Slides) Z. Zhang, Z. Zhu, X. Zhang (College of William and Mary) **Predictor-Directed Stream Buffers** (Presentation Slides) T. **Sherwood**, S ...

URL: www.microarch.org/micro33/advance_program.html - show in clusters

Sources: Looksmart 6, MSN 12, Lycos 10, Netscape 20

3. Predictor - Directed Stream Buffers [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

Predictor - Directed Stream Buffers Timothy **Sherwood** , Suleyman Sair, and Brad ... form of data prefetching, **stream buffers** , has been shown to be particularly ... In this paper we propose **Predictor** ...

URL: www-cse.ucsd.edu/~calder/abstracts/MICRO-00-PSB.html - show in clusters

Sources: Lycos 1, MSN 2

4. A Decoupled Predictor-Directed Stream Prefetching Architecture [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Architecture Suleyman Sair, Timothy **Sherwood**, Brad Calder , IEEE Abstract—An effective ... we propose **Predictor-Directed Stream Buffers** (PSB), which allows the **stream buffer** to follow a ...

URL: www.computer.org/tc/tc2003/t0260abs.htm - show in clusters

Sources: Looksmart 2, MSN 4

5. Friends of SimpleScalar LLC [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... Reorder **Buffer** Architecture, in ... Branch Target **Predictor** to Reduce Power ... Compiler-Directed Dynamic Voltage ... Sair, Timothy **Sherwood**, and Brad ... Quantifying Load **Stream** Behavior, in the ...

URL: www.simplescalar.com/friends.html - show in clusters

Sources: Looksmart 5, MSN 11, Lycos 12

6. MICRO-33 Program on CD [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... based Page Interleaving Scheme to Reduce Row-buffer Conflicts and Exploit Data Locality (PDF / PS ... of William and Mary) **Predictor-Directed Stream Buffers** (PDF / PS) Timothy **Sherwood**, Suleyman Sair ...

URL: www.capsl.udel.edu/COMPILER/MICRO33/m33pgm.htm - show in clusters

Sources: Looksmart 4, MSN 10

7. Comp.compilers: MICRO-33 Advance Program [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... symposium workshops - Feedback- **Directed** and Dynamic Optimization ... ACM Workshop on Feedback- **Directed** and Dynamic Optimization ... William and Mary) **Predictor - Directed Stream Buffers** Timothy...

URL: compilers.iecc.com/comparch/article/00-10-229 - show in clusters

Sources: Lycos 7, Looksmart 8

8. University of California, Irvine [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... **Predictor - Directed Stream Buffers** (PSB), ... Markov (SFM) predictor to direct stream . buffer prefetching and ... Timothy **Sherwood**, Suleyman Sair, and Brad Calder, **Predictor - Directed Stream** ...

URL: www.ics.uci.edu/~rgupta/darpa-memarch/quarterlys/q101.pdf - show in clusters

Sources: MSN 7, Netscape 13

9. Predictor-Directed Stream Buffers [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

Predictor-Directed Stream Buffers Timothy **Sherwood**, Suleyman Sair, and Brad Calder In proceedings of ... we propose **Predictor-Directed Stream Buffers** (PSB), a scheme in which the **stream buffer** follows an ...

URL: www.cs.ucsd.edu/users/calder/abstracts/MICRO-00-PSB.html - show in clusters

Sources: Looksmart 1

10. Citations: Predictor - Directed Stream Buffers - Sherwood, Sair ... [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... When performing the instruction cache fetch, the prefetch **buffer** is T. **Sherwood** , S. Sair, and B. Calder.

Predictor - directed stream buffers . . .

URL: citeseer.nj.nec.com/context/1854381/434945 - show in clusters
 Sources: Netscape 2

11. [www.cs.technion.ac.il ...P31](http://www.cs.technion.ac.il/~cs236603/PapresClass/P31.pdf) [new window] [frame] [preview]

Predictor - Directed Stream Buffers Timothy **Sherwood** Suleyman ... this paper we propose **Predictor - Directed Stream Buffers (PSB)**, a scheme in which ... **stream buffer** called the **Predictor** ...

URL: www.cs.technion.ac.il/~cs236603/PapresClass/P31.pdf - show in clusters
 Sources: Lycos 2

12. [MICRO 2000](http://www.informatik.uni-trier.de/.../db/conf/micro/micro2000.html) [new window] [frame] [preview]

... scheme to reduce **row-buffer** conflicts and exploit data locality. 32-41 Electronic Edition (link) Timothy **Sherwood**, Suleyman Sair, Brad Calder: **Predictor-directed stream buffers**. 42-53 Electronic ...

URL: www.informatik.uni-trier.de/.../db/conf/micro/micro2000.html - show in clusters
 Sources: Looksmart 7, MSN 22

13. [Predictor - directed stream buffers](http://portal.acm.org/...M&coll=GUIDE&CFID=11111111&CFTOKEN=2222222) [new window] [frame] [preview]

... **Predictor - directed stream buffers** . . . S. Palacharla , RE Kessler, Evaluating **stream buffers** as a ... Brad Calder , Todd Austin, Fetch **directed** instruction prefetching ...

URL: portal.acm.org/...M&coll=GUIDE&CFID=11111111&CFTOKEN=2222222 - show in clusters
 Sources: Netscape 3

14. [A Decoupled Predictor - Directed Stream Prefetching Architecture](http://www-cse.ucsd.edu/~calder/abstracts/IEEE-TC-02-PDSB.html) [new window] [frame] [preview]

A Decoupled **Predictor - Directed Stream** Prefetching Architecture Suleyman Sair, Tim **Sherwood**, and Brad Calder IEEE Transactions on Computers, 2002. An effective method for reducing the effect of ...

URL: www-cse.ucsd.edu/~calder/abstracts/IEEE-TC-02-PDSB.html - show in clusters
 Sources: MSN 3

15. [sosp16.cs.washington.edu ...Thesis.book](http://sosp16.cs.washington.edu/...Thesis.book) [new window] [frame] [preview]

... second-level cache miss **stream** . Accessing data from main ... reconfirm the ability of **stream buffers** to prefetch effectively ... 5.3 Non-strided Address **Streams** ... 55 4.5.4 Hybrid **Predictor** ...

URL: SOSP16.cs.washington.edu/homes/waynew/papers/Thesis.book.pdf - show in clusters
 Sources: Lycos 3

16. [Citations: Tolerating Memory Latency through Software-Controlled Pre-Execution](http://citeseer.com/context/1753042/489293) [new window] [frame] [preview]

... Quantifying Load **Stream** Behavior - Sair, **Sherwood**, Calder (2002) (Correct)a prefetching architecture that uses a **predictor directed stream buffer** to prefetch down data miss **streams** ...

URL: citeseer.com/context/1753042/489293 - show in clusters
 Sources: Looksmart 3

17. [Predictor - Directed Stream Buffers](http://www.cs.ucsd.edu/~calder/abstracts/MICRO-00-PSB.html) [new window] [frame] [preview]

Predictor - Directed Stream Buffers . Timothy **Sherwood** , Suleyman Sair, and Brad Calder In proceedings of the 33rd International Symposium ...

URL: www.cs.ucsd.edu/~calder/abstracts/MICRO-00-PSB.html - show in clusters
 Sources: Netscape 4

18. [systems.cs.colorado.edu ...](http://systems.cs.colorado.edu/.../Thesis-cooksey/cooksey-thesis.ps) [new window] [frame] [preview]

... Sensitive Data Prefetching Thesis **directed** by Associate Professor Dirk Grunwald ... 20 3.1.2 **Stream Predictors** ... 26 3.1.4 **Correlation Predictors** ...

URL: systems.cs.colorado.edu/.../Thesis-cooksey/cooksey-thesis.ps - show in clusters
 Sources: Lycos 4

19. [Predictor - Directed Stream Buffers](http://www.cs.ucsd.edu/.../2002-sherwood-gawk/gawk-tutorial.ppt) [new window] [frame] [preview]

AWK: The Duct Tape of Computer Science Research. Tim **Sherwood** . UC San Diego. AWK - **Sherwood** . 2. Duct Tape. Research Environment. Lots ...

URL: www.cs.ucsd.edu/.../2002-sherwood-gawk/gawk-tutorial.ppt - show in clusters
 Sources: Netscape 5

20. Predictor - Directed Stream Buffers [\[new window\]](#) [\[frame\]](#) [\[preview\]](#)

... California, Irvine **Predictor - Directed Stream Buffers** Timothy Sherwood, Suleyman Sair, Brad ... in the **stream buffer**) For indexing in address **predictor** , confidence information, local ...

URL: www.cecs.uci.edu/.../Predictor-Directed_Stream_Buffers.ppt - show in clusters

Sources: MSN 5

Result Pages: 1-20 - [21-40](#) - [41-58](#)

Details

Looksmart - Top 10 results retrieved, 95 requested. (5 pages requested - 5 OK)

Lycos - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

MSN - Top 23 results retrieved, 95 requested. (1 page requested - 1 OK)

Netscape - Top 20 results retrieved, 20 requested. (2 pages requested - 2 OK)

Overture - No result retrieved, 30 requested. (1 page requested - 1 OK)

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Check to search within this result set

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1 Database access characterization for buffer hit prediction

Dan, A.; Yu, P.S.; Chung, J.-Y.

Data Engineering, 1993. Proceedings. Ninth International Conference on , 19-21 April 1993

Pages:134 - 143

[\[Abstract\]](#) [\[PDF Full-Text \(808 KB\)\]](#) **IEEE CNF**

2 Memory access pattern analysis and stream cache design for multimedial applications

Junghee Lee; Chanik Park; Soonhoi Ha;

Design Automation Conference, 2003. Proceedings of the ASP-DAC 2003. Asia and South Pacific , 21-24 Jan. 2003

Pages:22 - 27

[\[Abstract\]](#) [\[PDF Full-Text \(707 KB\)\]](#) **IEEE CNF**

3 Real time Web usage mining with a distributed navigation analysis

Masseglia, F.; Teisseire, M.; Poncelet, P.

Research Issues in Data Engineering: Engineering E-Commerce/E-Business Systems, 2002. RIDE-2EC 2002. Proceedings. Twelfth International Workshop on , 24-25 Feb. 2002

Pages:169 - 174

[\[Abstract\]](#) [\[PDF Full-Text \(291 KB\)\]](#) **IEEE CNF**

4 Performance prediction for different consistency schemes in distributed shared memory systems

Srbljic, S.; Vranesic, Z.G.; Budin, L.

High Performance Distributed Computing, 1994., Proceedings of the Third IEEE

International Symposium on , 2-5 Aug. 1994
Pages:295 - 302

[\[Abstract\]](#) [\[PDF Full-Text \(676 KB\)\]](#) [IEEE CNF](#)

5 Design and evaluation of data access prediction strategies in SDSM systems

Pineschi, E.J.; de Castro, M.C.S.; de Amorim, C.L.;
Computer Architecture and High Performance Computing, 2002. Proceedings.
Symposium on , 28-30 Oct. 2002
Pages:151 - 158

[\[Abstract\]](#) [\[PDF Full-Text \(324 KB\)\]](#) [IEEE CNF](#)

6 A parallel processor architecture for prefetching

Kim, S.-M.; Manoharan, S.;
Parallel Architectures, Algorithms and Networks, 2000. I-SPAN 2000. Proceed
International Symposium on , 7-9 Dec. 2000
Pages:254 - 259

[\[Abstract\]](#) [\[PDF Full-Text \(528 KB\)\]](#) [IEEE CNF](#)

7 Timer management in X.25 layer 2-an interpretation

Khanna, V.K.;
TENCON '93. Proceedings. Computer, Communication, Control and Power
Engineering.1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993
Pages:540 - 543 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) [IEEE CNF](#)

8 Effective hardware-based data prefetching for high-performance processors

Tien-Fu Chen; Jean-Loup Baer;
Computers, IEEE Transactions on , Volume: 44 , Issue: 5 , May 1995
Pages:609 - 623

[\[Abstract\]](#) [\[PDF Full-Text \(1408 KB\)\]](#) [IEEE JNL](#)

9 Estimating and optimizing performance for parallel programs

Fahringer, T.;
Computer , Volume: 28 , Issue: 11 , Nov. 1995
Pages:47 - 56

[\[Abstract\]](#) [\[PDF Full-Text \(1416 KB\)\]](#) [IEEE JNL](#)

10 On parallelizing the EM algorithm for PET image reconstruction

Chung-Ming Chen; Soo-Young Lee;
Parallel and Distributed Systems, IEEE Transactions on , Volume: 5 , Issue:
8 , Aug. 1994
Pages:860 - 873

[\[Abstract\]](#) [\[PDF Full-Text \(1336 KB\)\]](#) [IEEE JNL](#)

11 Real time Web usage mining: a heuristic based distributed miner

Masseglia, F.; Teisseire, M.; Poncelet, P.;

Web Information Systems Engineering, 2001. Proceedings of the Second International Conference on , Volume: 1 , 3-6 Dec. 2001

Pages:288 - 297 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(886 KB\)\]](#) [IEEE CNF](#)

12 Tolerating memory latency through software-controlled pre-execution in simultaneous multithreading processors

Chi-Keung Luk;

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